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February 19, 2004

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**Facsimile Transmission**

5 pages including this page

**TO: Filing Receipt Corrections**

Fax No.: 1 703 746 9195

Phone No.: 1 703 308 1202

**RE: U.S. Patent Application No. 10/612 825**

PROCESS FOR RUNNING PROGRAMS WITH SELECTABLE INSTRUCTION  
LENGTH PROCESSORS AND CORRESPONDING PROCESSOR SYSTEM

**Our Reference: 851763.434**

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Second Request for Corrected Filing Receipt  
Filing Receipt with correction

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Fabrizio Simone Rovati et al.  
Application No. : 10/612,825  
Filed : July 1, 2003  
For : PROCESS FOR RUNNING PROGRAMS WITH SELECTABLE  
INSTRUCTION LENGTH PROCESSORS AND  
CORRESPONDING PROCESSOR SYSTEM

Art Unit : 2183  
Docket No. : 851763.434  
Date : February 19, 2004

Fax to 703-746-9195  
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Office of Initial Patent Examination  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SECOND REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents:

Attached is a copy of the official Filing Receipt received from the PTO in the above-identified application, for which issuance of a corrected Filing Receipt is respectfully requested.

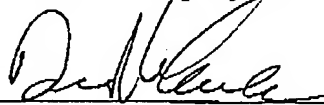
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There is an error in the Foreign Applications Data, which should read:

02425437.7 07/02/2002

The correction to be made has been marked on the attached copy of the Filing Receipt.

Respectfully submitted,  
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
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## Applicant(s)

Fabrizio Simone Rovati, Cinisello Balsamo, ITALY;  
Antonio Maria Borneo, Matèra, ITALY;  
Danilo Pietro Pau, Sesto San Giovanni, ITALY;

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JAN 26 2004

## Assignment For Published Patent Application

STMicroelectronics S.r.l., Agrate Brianza, ITALY;

Seed Intellectual Property  
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## Domestic Priority data as claimed by applicant

## Foreign Applications

02425437.7  
EUROPEAN PATENT OFFICE (EPO) 02425436.9 07/02/2002

If Required, Foreign Filing License Granted: 09/26/2003

Projected Publication Date: 03/25/2004

Non-Publication Request: No

Early Publication Request: No

Title

Process for running programs with selectable instruction length processors and corresponding processor system

Preliminary Class

712

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